

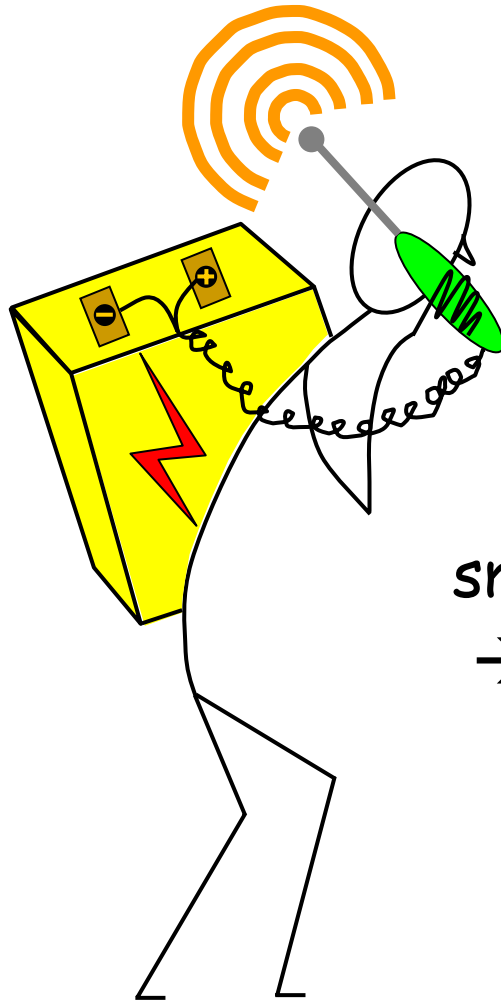
**6.002**

**CIRCUITS AND  
ELECTRONICS**

# Energy and Power

Cite as: Anant Agarwal and Jeffrey Lang, course materials for 6.002 Circuits and Electronics, Spring 2007. MIT OpenCourseWare (<http://ocw.mit.edu/>), Massachusetts Institute of Technology. Downloaded on [DD Month YYYY].

# Why worry about energy?



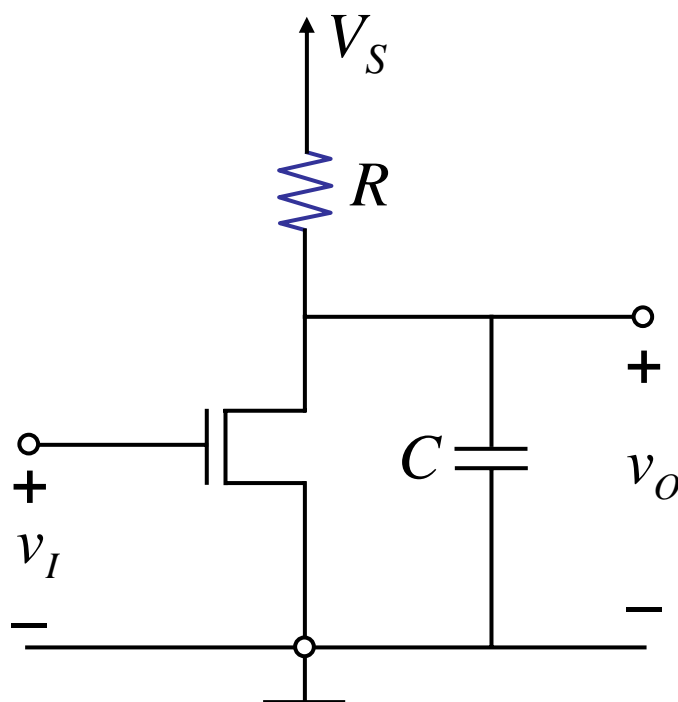
small batteries  
→ good

## Today:

- How long will the battery last?  
in standby mode  
in active use
- Will the chip overheat and self-destruct?

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# Look at energy dissipation in MOSFET gates



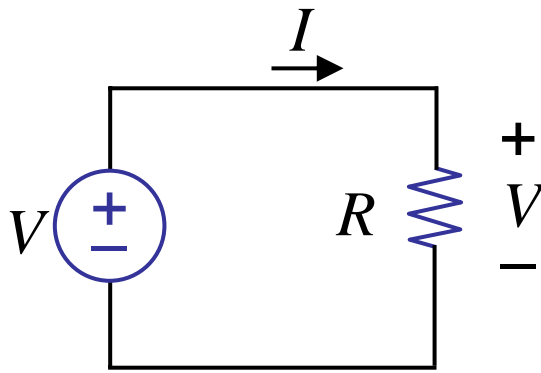
$C$ : wiring capacitance and  $C_{GS}$  of following gate

Let us determine  
standby power  
active use power

Let's work out a few related examples first.

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## Example 1:



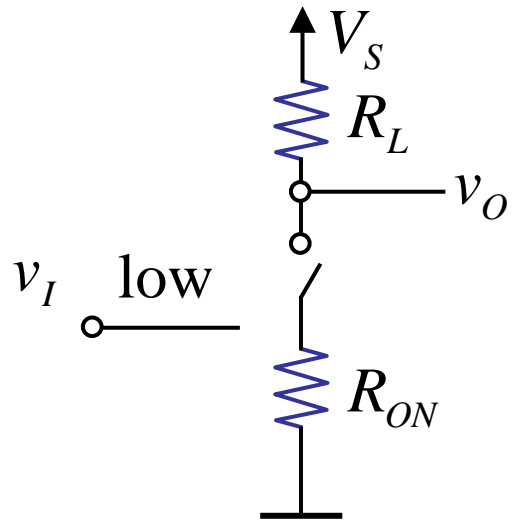
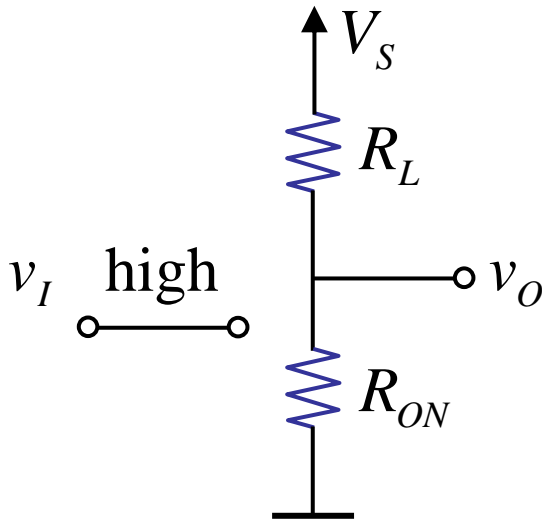
Power  $P = VI = \frac{V^2}{R}$

Energy dissipated in time  $T$

$$E = VIT$$

# Example 1:

for our gate



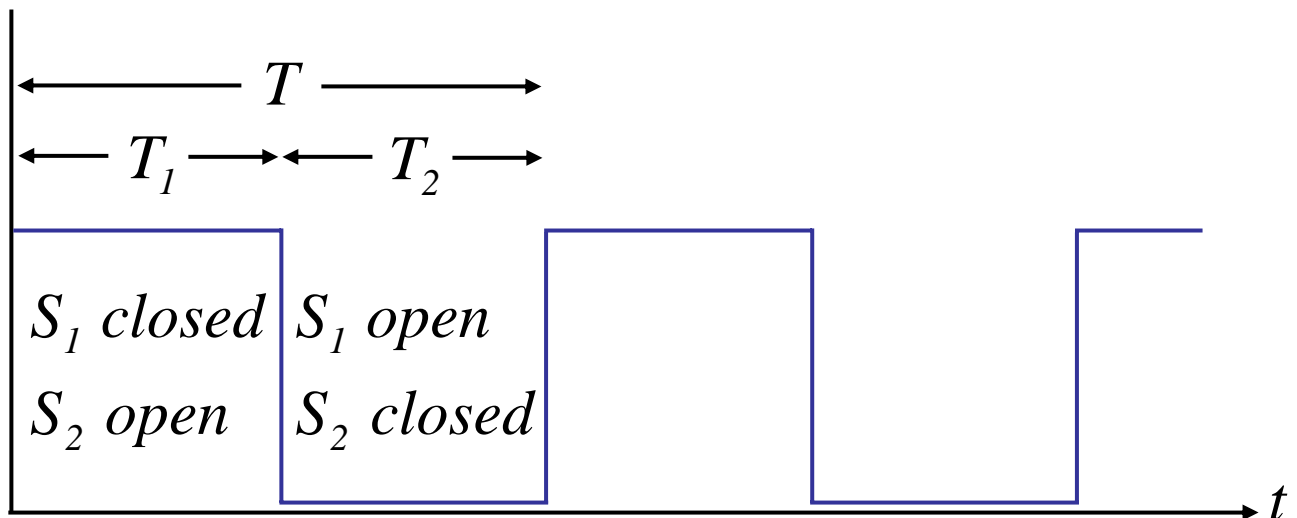
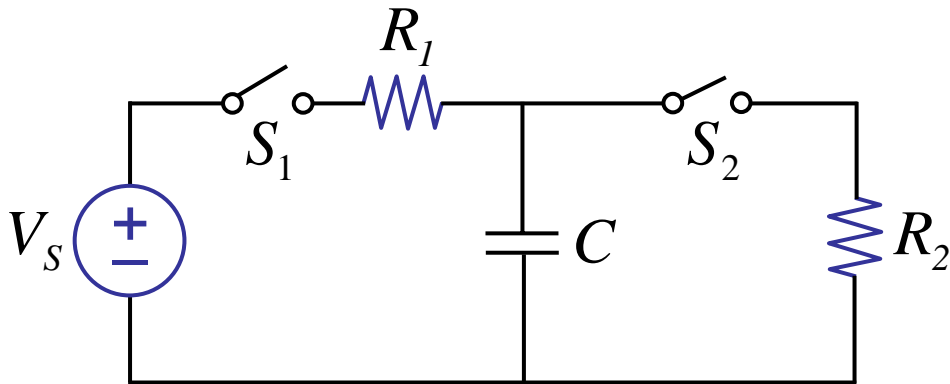
$$P = \frac{V_S^2}{R_L + R_{ON}}$$



$$P = 0$$

## Example 2:

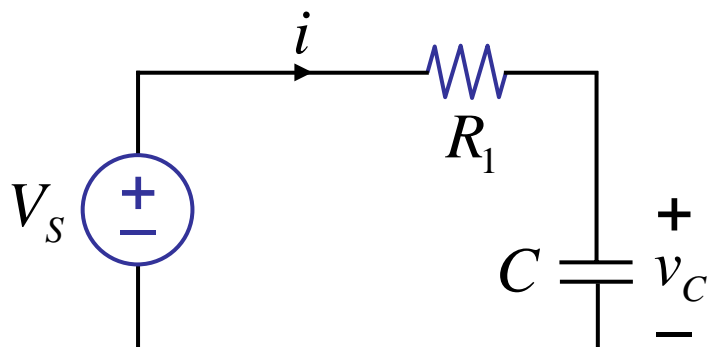
Consider



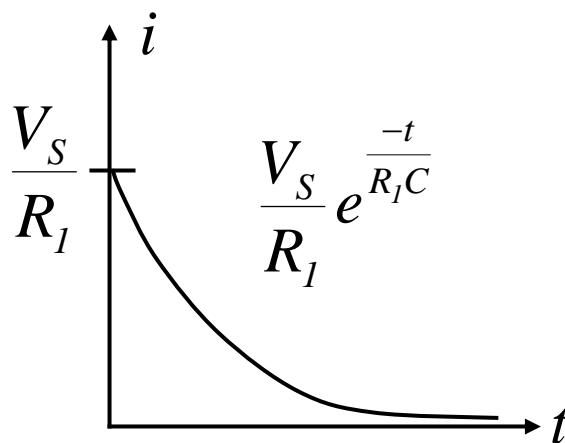
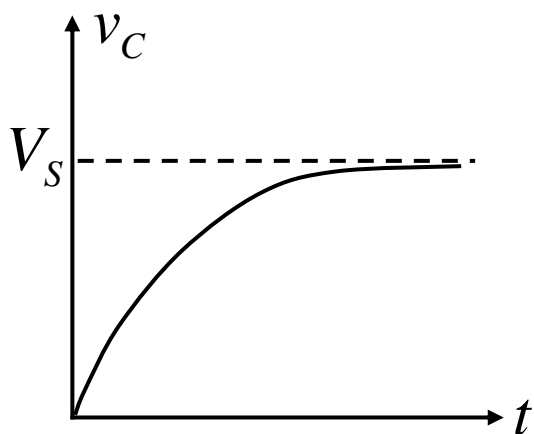
Find energy dissipated in each cycle.

Find average power  $\overline{P}$ .

$T_1$ :  $S_1$  closed,  $S_2$  open



assume  
 $v_C = 0$  at  $t = 0$



# Total energy provided by source during $T_1$

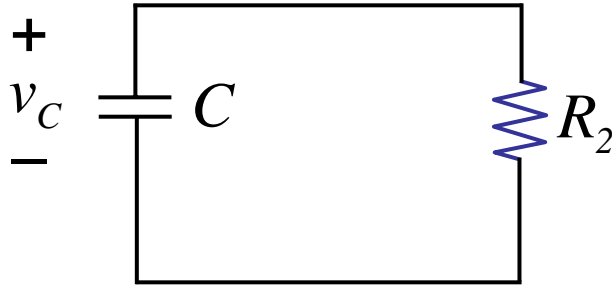
$$\begin{aligned} E &= \int_0^{T_1} V_S i \, dt \\ &= \int_0^{T_1} \frac{V_S^2}{R_1} e^{\frac{-t}{R_1 C}} \, dt \\ &= -\frac{V_S^2}{\cancel{R_1} C} e^{\frac{-t}{R_1 C}} \Big|_0^{T_1} \\ &= C V_S^2 \left( 1 - e^{\frac{-T_1}{R_1 C}} \right) \end{aligned}$$

$\approx C V_S^2$  if  $T_1 \gg R_1 C$   
I.e., if we wait long enough

$$\left. \begin{aligned} \frac{1}{2} C V_S^2 &\text{ stored on } C, \\ E_1 = \frac{1}{2} C V_S^2 &\text{ dissipated in } R_1 \end{aligned} \right\} \text{Independent of } R!$$



$T_2$ :  $S_2$  closed,  $S_1$  open



Initially,  $v_C = V_s$  (recall  $T_1 \gg R_1 C$ )

So, initially,

$$\text{energy stored in capacitor} = \frac{1}{2} C V_s^2$$

Assume  $T_2 \gg R_2 C$

So, capacitor discharges ~fully in  $T_2$

So, energy dissipated in  $R_2$  during  $T_2$

$$E_2 = \frac{1}{2} C V_s^2$$

$E_1, E_2$  independent of  $R_2$ !

# Putting the two together:

Energy dissipated in each cycle

$$E = E_1 + E_2$$

$$= \frac{1}{2}CV_s^2 + \frac{1}{2}CV_s^2$$

$$E = CV_s^2 \text{ energy dissipated in}$$

*charging & discharging C*

Assumes C charges and discharges fully.

Average power

$$\bar{P} = \frac{E}{T}$$

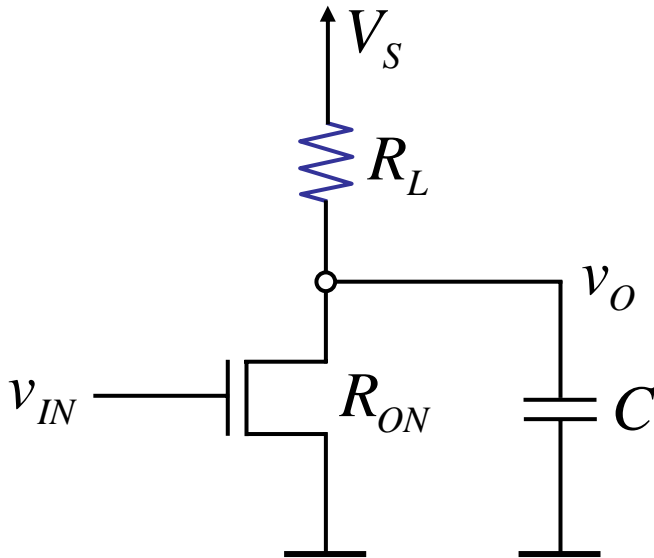
$$= \frac{CV_s^2}{T}$$

$$= CV_s^2 f$$

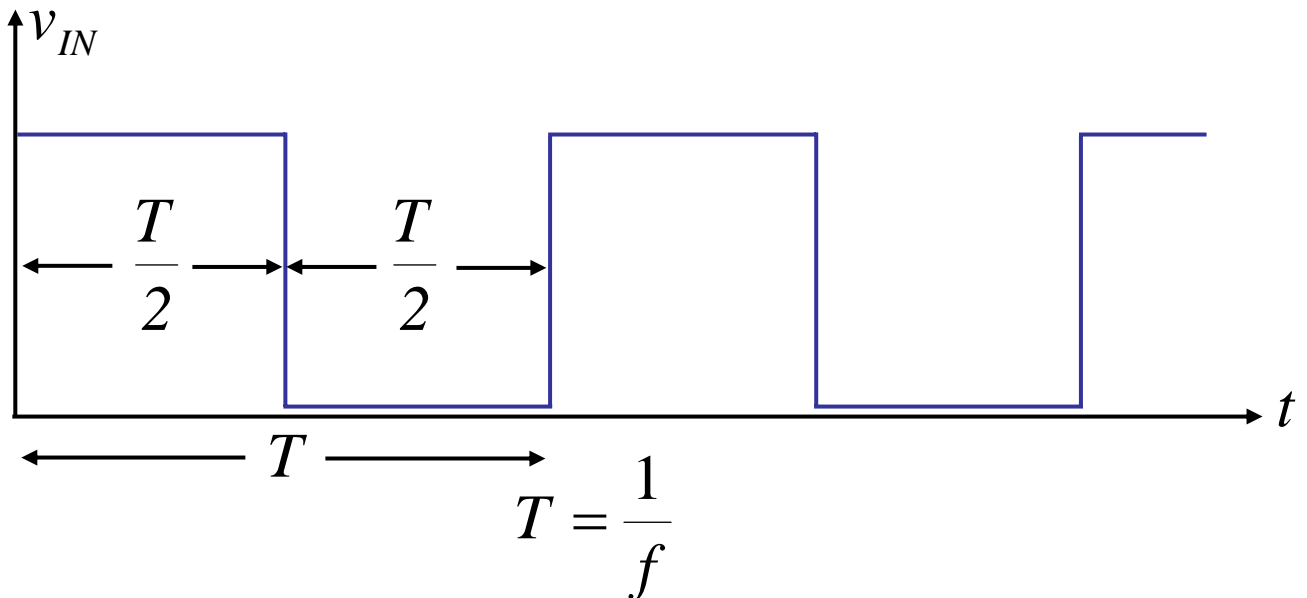
  
frequency  $f = \frac{1}{T}$

*(A blue arrow points from the text 'frequency' to the variable 'f' in the equation above.)*

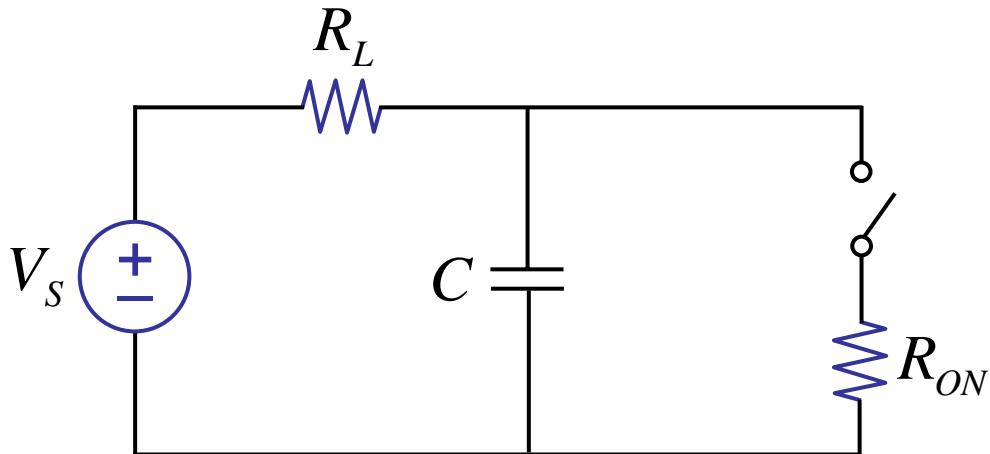
# Back to our inverter —



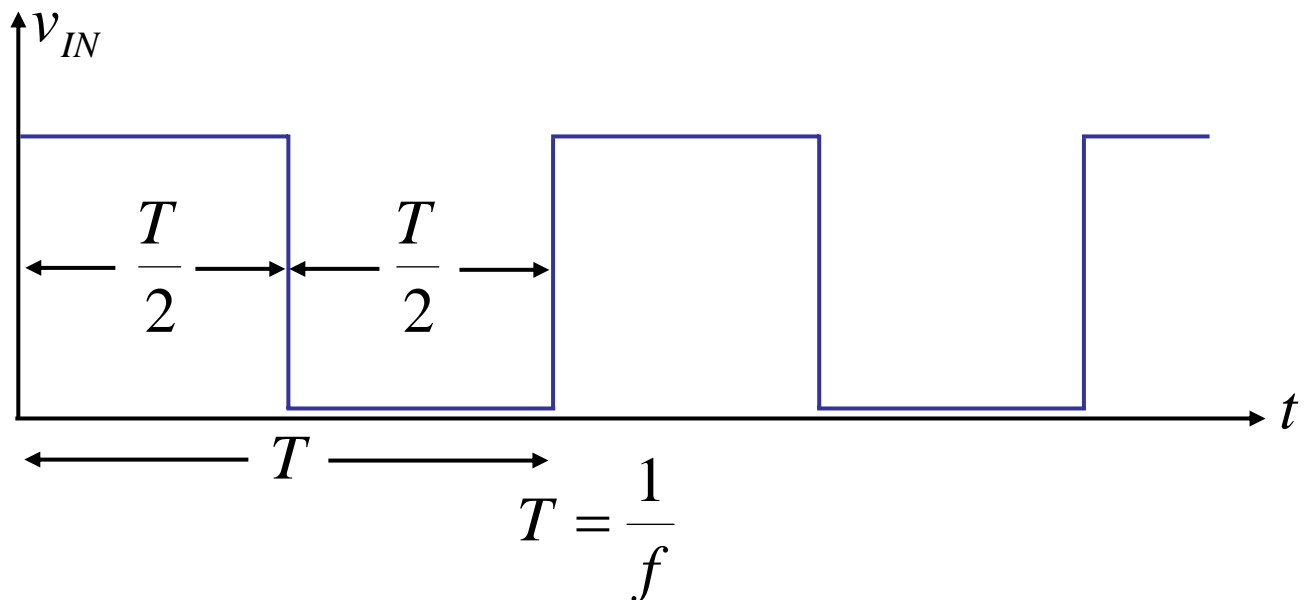
What is  $\bar{P}$  for the following input?



# Equivalent Circuit



What is  $\bar{P}$  for the following input?



# What is $\bar{P}$ for gate?

We can show (see section 12.2 of A & L)

$$\bar{P} = \frac{V_S^2}{2(R_L + R_{ON})} + CV_S^2 f \frac{R_L^2}{(R_L + R_{ON})^2}$$

when  $R_L \gg R_{ON}$

$$\bar{P} = \frac{V_S^2}{2R_L} + CV_S^2 f$$

**remember** ★

$\bar{P}_{STATIC}$

independent of  $f$ .  
MOSFET ON half  
the time.

**remember** ★★

$\bar{P}_{DYNAMIC}$

related to switching  
capacitor

# What is $\overline{P}$ for gate?

when  $R_L \gg R_{ON}$

$$\overline{P} = \frac{V_S^2}{2R_L} + CV_S^2 f$$

In standby mode,  
half the gates in a  
chip can be  
assumed to be on.

So  $\overline{P}_{STATIC}$  per  
gate is still  $\frac{V_S^2}{2R_L}$ .

Relates to standby  
power.

In standby mode,  
 $f \rightarrow 0$ ,  
so dynamic power  
is 0

# Some numbers...

a chip with  $10^6$  gates clocking  
at 100 MHz

$$C = 1fF$$

$$R_L = 10k\Omega$$

$$f = 100 \times 10^6$$

$$V_S = 5V$$

$$\bar{P} = 10^6 \left[ \frac{25}{2 \times 10^4} + 10^{-15} \times 25 \times 100 \times 10^6 \right]$$

$$= 10^6 [1.25 \text{ milliwatts} + 2.5 \text{ microwatts}]$$

**problem!**

1.25KW!

2.5W

not bad

must get rid of this

$$\propto V_S^2$$

$$\propto f$$

reduce  $V_S$

$$5V \rightarrow 1V$$

$$2.5W \rightarrow 150mW$$

