Computer-Aided Algorithm Design: Automated Tuning, Configuration, Selection and Beyond

Holger H. Hoos

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- roll up your sleeves and do the best you can



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- use your little grey cells, then your little black chips





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 \rightsquigarrow principled experimentation + generic techniques

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- enables effective exploration of larger design spaces
- facilitates principled design of heuristic algorithms
- profoundly changes how we build and use algorithms

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- various heuristic components interact in complex ways
 various unexpected, emergent behaviour
- performance can be tricky to assess due to
 - differences in behaviour across problem instances
 - stochasticity

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- resulting algorithms often complex, somewhat ad-hoc, not fully optimised

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- ► Given: High-performance DPLL-type SAT solver (SPEAR)
 - 26 parameters (7 categorical, 3 Boolean, 12 continuous, 4 integer-valued)
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- Goal: Minimize expected run-time on 'typical' SAT instances from software verification tool
- Problems:
 - default settings $\rightsquigarrow \approx$ 300 seconds / run
 - good performance on some instances may not generalise

Outline

1. Introduction

- 2. From traditional to computer-aided algorithm design
- 3. Design spaces and design patterns
- 4. Meta-algorithmic search and optimisation procedures
- 5. Three success stories (SAT, timetabling, MIP)
- 6. The next step: Programming by Optimisation

Traditional algorithm design approach:

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- design often starts from generic or broadly applicable problem solving method (*e.g.*, evolutionary algorithm)

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- Design decisions interact in complex ways.

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- \rightsquigarrow complicated designs, unfulfilled performance potential

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- → genetic programming, hyper-heuristics, reactive search; learning and intelligent optimisation, SLS engineering; meta-learning; program synthesis

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Meta-algorithmic system:

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- evaluates candidate design
- finds high-performance designs

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- uses principled, fully formalised methods for algorithm design
- can be used to customise algorithms for use in specific applications with minimal human effort

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- manual configuration by algorithm designer
- automated configuration using ParamILS, a generic algorithm configuration procedure Hutter, HH, Stützle (2007)

Spear : Empirical results on software verification benchmarks

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- ► ≈ 500-fold speedup through use automated algorithm configuration procedure (ParamILS)
- new state of the art

(winner of 2007 SMT Competition, QF_BV category)

Design spaces and design patterns

Special cases of computer-aided algorithm design:

parameter optimisation (for given set of instances)
 Birattari *et al.* (2002); Adenso-Diaz & Laguna (2006),

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restart strategies

Luby *et al.* (1993); Gagliolo & Schmidhuber (2007); Streeter *et al.* (2007)

instance-based algorithm configurators

Hutter et al. (2006); Malitsky & Sellmann (2009)

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\rightsquigarrow meta-algorithmic design patterns, induce design spaces

Meta-algorithmic search and optimisation procedures

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 use powerful heuristic search and optimisation procedures, combined with significant amounts of computing power

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- use powerful heuristic search and optimisation procedures, combined with significant amounts of computing power
- use machine learning methods (classification, regression), combined with significant amount of training data

Some examples:

parameter tuning:

numerical optimisation techniques
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- algorithm configuration:
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 - racing procedures
 e.g., F-Race (Birattari et al. 2002)
 - advanced stochastic local search procedures e.g., ParamILS (Hutter et al. 2007)

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- dynamic algorithm portfolios (time allocators)
 - bandit solvers (e.g., Gagliolo & Schmidhuber 2007)
 - evolutionary algorithms (e.g., Harik & Lobo 1999)

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- How to best deal with censored, sparse data?

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- Propositional Satisfiability
- Course Timetabling
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Further successes:

- protein structure prediction (Thachuk et al. 2007)
- SAT (KhudaBukhsh *et al.* 2009; Xu *et al.* to appear; Tompkins & HH – to appear)
- TSP (Styles & HH in preparation)

Holger Hoos: Computer-aided algorithm design

Xu, Hutter, HH, Leyton-Brown (2008)

Key idea: Instance-based Algorithm Selection (Rice 1976)

- Given: set S of algorithms for a problem, problem instance π
- Select from S the algorithm expected to solve π most efficiently, based on (cheaply computable) features of π.

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▶ CNF formula ~→ 84 polytime-computable instance features

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SATzilla in a nutshell:

- ▶ CNF formula ~→ 84 polytime-computable instance features
- ► features ~→ performance prediction for set of SAT solvers
- run solver with best predicted performance

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 Use state-of-the-art complete (DPLL) and incomplete (local search) SAT solvers.

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- Use state-of-the-art complete (DPLL) and incomplete (local search) SAT solvers.
- Use ridge regression on selected features to predict solver run-times from instance features.
- Use method by Schmee & Hahn (1979) to deal with censored run-time data.

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 \sim prizes in 5 of the 9 main categories of the 2009 SAT Solver Competition (3 gold, 2 silver medals)

Post-Enrolment Course Timetabling

Chiarandini, Fawcett, HH (2008); Fawcett, HH, Chiarandini (in preparation)

Post-Enrolment Course Timetabling:

- students enroll in courses
- courses are assigned to rooms and time slots, subject to hard constraints
- preferences are represented by soft constraints

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Our solver:

- modular multiphase stochastic local search algorithm
- hard constraint solver: finds feasible course schedules
- soft constraint solver: optimise schedule (maintaining feasibility)

Our first solver:

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- starting point: Chiarandini et al. (2003)
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- 7 parameters, 50 400 configurations

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Automated configuration process:

- configurator: FocusedILS 2.3 (Hutter et al. 2009)
- ▶ performance objective: solution quality after 300 CPU sec

2nd International Timetabling Competition (ITC), Track 2



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Automated configuration process:

- configurator: FocusedILS 2.4 (new version, multiple stages)
- multiple performance objectives (final stage: solution quality after 600 CPU sec)

2-way race against ITC Track 2 winner



2-way race against ITC Track 2 winner



- our solver wins beats ITC winner on 20 out of 24 competition instances
- application to university-wide exam scheduling at UBC (≈ 1650 exams, 28 000 students)

Holger Hoos: Computer-aided algorithm design
Mixed Integer Programming (MIP)

Hutter, HH, Leyton-Brown, Stützle (2009); Hutter, HH, Leyton-Brown (2010)

- MIP is widely used for modelling optimisation problems
- MIP solvers play an important role for solving broad range of real-world problems

CPLEX:

- prominent and widely used commercial MIP solver
- exact solver, based on sophisticated branch & cut algorithm and numerous heuristics
- ▶ 159 parameters, 81 directly control search process

[CPLEX 12.1 user manual, p. 478]

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CPLEX on various MIPS benchmarks

Benchmark	Default performance	Optimised performance	Speedup
	[CPU sec]	[CPU sec]	factor
BCOL/CONIC.SCH	5.37	$2.35~(2.4\pm 0.29)$	2.2
BCOL/CLS	712	$23.4~(327\pm 860)$	30.4
BCOL/MIK	64.8	$1.19~(301\pm 948)$	54.4
CATS/Regions200	72	$10.5~(11.4\pm 0.9)$	6.8
RNA-QP	969	525 (827 \pm 306)	1.8

(Timed-out runs are counted as 10 \times cutoff time.)

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CPLEX on BCOL/CLS



CPLEX on BCOL/CONIC.SCH



Latest results: Gurobi on BCOL/MIK



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Latest results: Ipsolve on CA-WDP



Latest results: Ipsolve on CA-WDP



application context

application context

+ design space

application context + design space + optimisation procedure

application context + design space + optimisation procedure + compute power

application context +design space +optimisation procedure compute power success

The next step: Programming by Optimisation

How to easily use computer-aided algorithm design?

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How to easily use computer-aided algorithm design?

Need effective support for ...

specification of rich design spaces

The next step: Programming by Optimisation

How to easily use computer-aided algorithm design?

Need effective support for ...

- specification of rich design spaces
- automated design (and analysis) process

Nell, Fawcett, HH, Leyton-Brown (under review)

support algorithm design and empirical analysis

- support algorithm design and empirical analysis
- support wide range of design patterns, procedures

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- web-based UI, component-based architecture
- open source, easy to use & expand

ILAL 1.0	 	
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Evaluate algorithm performance

Analyse performance of an algorithm on an instance set.

Compare algorithm performance

Compare the performance of two algorithms on an instance set.

Configure algorithm

Optimize parameter settings to maximize algorithm performance on an instance set.

	<u>.</u>
	•

Status ID

99%

97%

8%

Start Time

1q/3r

CPU Time

0.8

2010-04-02 17:25:05.0 258122.70 s KILL

2010-04-02 17:31:31.0 253904.81 s KILL

Completed Tasks

Active Tasks

N/A

6 Compare GGA/PILS SATenstein 2010-04-05 08:47:26.0 1322.16 s KILL

Name

queued 5 Compare GGA/PILS SPEAR

4 GGA SPEAR SWV

3 ParamILS SPEAR SWV

Status	D	Name	Start Time	CPU Time
done	1	ParamILS SATenstein QCP	2010-04-02 15:07:35.0	188920.02 s
done	2	GGA SATenstein QCP	2010-04-02 15:08:41.0	181342.54 s

New Algorithm Configuration Task

Target Algorithm

Choose a target algorithm to configure

SPEAR \$ New Algorithm

Configuration Space

Choose the Configuration Space for the target Algorithm

Full Configuration Space + New Configuration Space

Problem Instances

Choose an instance set to use for training

SWV-Train \$ New Instance Set

Configurator

Choose a configurator to run

ParamILS2.3.3 \$

Default Configurator Settings + New Configurator Settings

Execution Environment

Choose an execution environment to use

Arrow Cluster, single-node	\$)(New Execution Environment
----------------------------	------	---------------------------

Task Name: pILS SPEAR-SWV

Run

ILAL 1.0	 	
	 <u></u>	

Now	To	c la c
1101		101010

Evaluate algorithm performance

Analyse performance of an algorithm on an instance set.

Compare algorithm performance

Compare the performance of two algorithms on an instance set.

Configure algorithm

Optimize parameter settings to maximize algorithm performance on an instance set.

	<u>.</u>
	•

Status ID

99%

97%

8%

Start Time

1q/3r

CPU Time

0.8

2010-04-02 17:25:05.0 258122.70 s KILL

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Key idea:

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 generic programming language extension
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Programming by Optimisation (PbO)

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Key idea:

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 generic programming language extension
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 HAL + compute power

planner













Recent example: Hydra SAT solver (Xu et al. – to appear)

► automated construction of the solver (using ParamILS, SATzilla): ≈ 70 CPU days

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 - 2:45 hours at minimum wage

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Holger Hoos: Computer-aided algorithm design

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- liberates human designers from boring, menial tasks and lets them focus on higher-level design issues
- enables effective exploration of larger design spaces
- facilitates principled design of heuristic algorithms
- profoundly changes how we build and use algorithms

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Computing resources:

- Arrow, BETA, ICICS clusters
- WestGrid